

EXPEDITED PROCEDURE – EXAMINING GROUP 2111

S/N 09/644,463

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Matthew B. Haycock et al.	Examiner:	Raymond Phan
Serial No.:	09/644,463	Group Art Unit:	2111
Filed:	August 23, 2000	Docket No.:	884.303US1
Title:	SIMULTANEOUS BIDIRECTIONAL PORT WITH SYNCHRONIZATION CIRCUIT TO SYNCHRONIZE THE PORT WITH ANOTHER PORT		
Assignee:	Intel Corporation	Customer Number:	21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the final office action dated 12 June 2006, the applicants request reconsideration of the above-identified application in view of the following.

OK TO ENTER
8/18/02 Rg